

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoshida, et al

Serial No.: 08/929,263

Filed: 09/11/97

Docket No. TIJ-23462

Art Unit: 1104

Examiner: TBD

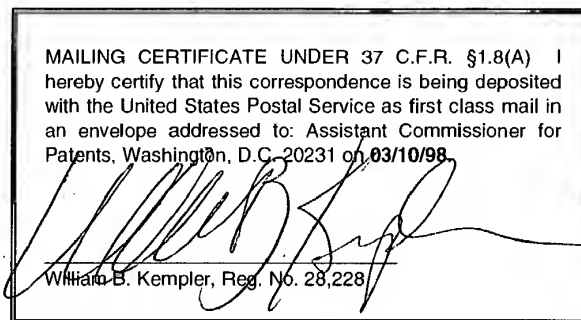
Title: FLASH EEPROM MEMORY ARRAY HORIZONTALLY DIVIDED INTO SECTORS
FOR ERASURE OPERATIONS



03/10/98

CHANGE OF CORRESPONDENCE ADDRESS

Assistant Commissioner for Patents
Washington, D. C. 20231



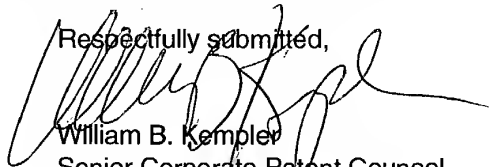
Sir:

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Respectfully submitted,


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